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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR			OR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/753,357	01/09/2004	Koji Yamaguc				118292	6391	
METHOD FOR DESIGNI MANUFACTURING SEMI APPLN. TYPE	NG WIRING SUBSTRAT. ICONDUCTOR MODULE SMALL ENTITY	E, METHOD FO		Т	G SEMICONDUCTO	OR DEVICE, AND METH	DATE DUE	
nonprovisional	NO	\$1400		1	\$300	\$1700	08/08/2006	
EXAMINER		ART UNIT		CLA	SS-SUBCLASS]		
POTTER, ROY KARL		2822		- :	257-778000	J		
1. Change of correspondence CFR 1.363).	e address or indication of "Fe	ee Address" (37	2. For prin	nting on th	e patent front page, li			
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(A) NAME OF ASSIGN	EE		(B) RESIDE	ENCE: (CI	TY and STATE OR (COUNTRY)		
Seiko Epson Corporation			Tokyo, Japan					
Please check the appropriate	assignee category or catego	ries (will not be pr	inted on the p	oatent):	☐ Individual ② Co	orporation or other private gr	oup entity	

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Authorized Signature Typed or printed name Gang Luo

GS/13/23ES MBEYEKE2 G33931GS 10753357 June 12, 2006 50,559

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